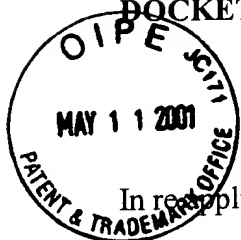


2183

DOCKET NO. 00-BN-051 (STMI01-00051)

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re application of : Anthony X. Jarvis, et al.
U.S. Serial No. : 09/751,372
Filed : December 29, 2000
For : SYSTEM AND METHOD FOR EXECUTING VARIABLE
LATENCY LOAD OPERATIONS IN A DATA PROCESSOR
Group No. : 2183
Examiner : (Not Yet Known)

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

CERTIFICATE OF MAILING

I hereby certify that this correspondence, including the attachments listed, is being deposited with the United States Postal Service, via First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231 on 5/31/01 (Date)

KATHY LONGENECKER
Printed Name of Person Mailing
Kathy Longenecker
Signature of Person Mailing

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Please direct future correspondence and telephone calls relating to this patent application to the following:

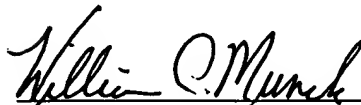
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